

**SYSTEM AND METHOD FOR RANK SPECIFIC TIMING  
ADJUSTMENTS FOR DOUBLE DATA RATE (DDR) COMPONENTS**

**ABSTRACT OF THE DISCLOSURE**

**[0036]** In some embodiments, a system and method for making rank-specific adjustments to the READ tenure parameters of a double data-rate (DDR) memory component to improve the DDR bus timing margins. When a READ tenure is encountered for the DDR memory component, the rank of the DDR memory component is calculated and the value is used to retrieve two adjustment signals, which are specific to the DDR memory component, from a look up table. One of the adjustment signals is used to adjust a gating signal for the data strobe signal of the component. The other adjustment signal is used to fine tune a required  $\frac{1}{4}$  clock delay for the data strobe signal to read the data from the DDR memory component while adjusting for the inherent latency of the DDR memory component. Other embodiments are described and claimed.